Title: Sparsity Based Ultrasound Imaging System

Number of Project Assistant Positions: 2

Salary per month: as per govt. norms, qualifications, and experience

Desired Qualifications:

Project Assistant – 1 (Signal Processing): B.Tech./M.Tech. in ECE, EE, Instrumentation Engineering, or Biomedical Engineering with proficiency in Matlab/Python programming and with a good knowledge of Signal Processing.

Interested candidates may send in their CV in PDF only by email to:

manjulam@iisc.ac.in for Project Assistant – 1 (Signal Processing) position with subject line: SRC Ultrasound Engineer – 1

on or before **October 15, 2021**.

Project Assistant – 2 (Hardware Design): B.Tech./M.Tech. in ECE, EE, Instrumentation Engineering with

-Hands-on knowledge of developing a complete FPGA based SoC design

-PCB Design

-Experience in the high-speed FPGA domain

-HW/SW Embedded System Design

-Industrial experience is preferable

Interested candidates may send in their CV in PDF only by email to:

sushmithav@iisc.ac.in for Project Assistant – 2 (Hardware Design) position with subject line: SRC Ultrasound Engineer – 2

on or before **October 15, 2021**.

The project is funded by Semiconductor Research Corporation (SRC). Our industry partner is Texas Instruments, Bangalore. The project will go on until March 31, 2023. There will be plenty of opportunity for collaboration, publication, and patenting.